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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10056770	FILING DATE 01/24/2002	CLASS 219	SUBCLASS 703	GAU 1752	EXAMINER 2878 LONG TRAN
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**APPLICANTS: Ferber Gottfried; Pelmer Reimund;

**CONTINUING DATA VERIFIED: N UKT

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** FOREIGN APPLICATIONS VERIFIED:

GERMANY 101 03 084.3 01/24/2001 Y UKT

PG-PUB DO NOT PUBLISH ☐

RESCIND ☐

Foreign priority claimed ☒ yes ☐ no
35 USC 119 conditions met ☒ yes ☐ no
Verified and Acknowledged Examiners's initials UKT

ATTORNEY DOCKET NO

WMP-EUP-008

TITLE : Semiconductor module and method for fabricating the semiconductor module

U.S. DEPT. OF COMM / PAT & TM-PTO-436 (Rev. 12-94)

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED		
			Total Claims	Print Claim for O.G.	
ISSUE FEE			DRAWING		
Amount Due	Date Paid	Primary Examiner	Sheets Drwg.	Figs. Drwg.	Print Fig.
			Application Examiner		
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.		

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